

ABSTRACT

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A configurable integrated circuit and method for trimming a desirable combination of analog and digital functions and customizing pin assignments by programming nonvolatile memory. Functional enhancement and chip compatibility are achieved by configuring an integrated circuit with specified analog and digital settings for operation with an external chip. Multiple NV registers with designated bits that correspond to activating, deactivating, trimming, or changing analog functions including internal voltages, bandgap, and oscillating frequencies.